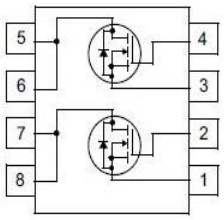
60V N-Channel Power MOSFET

• General Description

It combines trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

• Product Summary



V_{DS}= 60V

$R_{DS(ON)}$ = 28mΩ

I_D= 5.5A

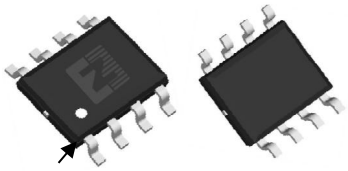
• Features

■AEC-Q101 Qualified

■Low $R_{DS(ON)}$ to minimize conductive loss

■Low Gate Charge for fast switching

■Low Thermal resistance



SOP-8

RoHS

REACH COMPLIANT

HF

•Application

■BLDC Motor driver

■DC-DC

■Battery protection

•Ordering Information:

Part NO.	ZMDA68602S
Marking	ZMD68602
Packing Information	REEL TAPE
Basic ordering unit (pcs)	4000

•Absolute Maximum Ratings (T_C=25℃)

Parameter	Symbol	Conditions	Value	Unit
Drain-Source Voltage	V_{DS}		60	V
Gate-Source Voltage	V_{GS}		±20	V
Continuous Drain Current	I_D	T_C=25℃	5.5	A
	I_D	T_C=75℃	5.5	A
	I_D	T_C=100℃	5.5	A
Pulsed Drain Current	I_{DM}	Pulsed; t_p ≤ 10 μs; T_{mb} = 25 °C;	22	A
Total Power Dissipation	P_D	T_C=25℃	6	W
Total Power Dissipation	P_D	T_A=25℃	2.1	W
Operating Junction Temperature	T_J		-55 to +150	℃
Storage Temperature	T_{STG}		-55 to +150	℃
Single Pulse Avalanche Energy	E_{AS}	L=0.1mH, V_{GS}=10V, Rg=25Ω,	25	mJ
		L=0.5mH, V_{GS}=10V, Rg=25Ω,	45	mJ
ESD Level (HBM)			CLASS 1B	

●Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}		-	20	°C/W
Thermal resistance, junction-ambient①	R_{thJA}		-	60	°C/W
Soldering temperature	Tsold		-	260	°C

●Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS} =0V, I_D =250uA	60			V
Gate Threshold Voltage	$V_{GS(TH)}$	V_{GS} =V_{DS}, I_D =250uA	1.2	1.8	2.5	V
Drain-Source Leakage Current	I_{DSS}	V_{GS}=0V, V_{DS}= 60V			1.0	uA
Gate- Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS} = 0V			100	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D= 12A		28	36	mΩ
		V_{GS}=4.5V, I_D= 8A		34	42	mΩ
Forward Transconductance	g_{FS}	V_{DS} =5V, I_{SD} = 5A		5		s
Diode Forward Voltage	V_{FSD}	V_{GS} =0V, I_{SD} = 12A			1.3	V

●Dynamic characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	Ciss	f = 1MHz, V_{DS}=25V	-	1300	-	pF
Output capacitance	Coss		-	53	-	
Reverse transfer capacitance	Crss		-	31	-	
Gate Risistance	Rg	f = 1MHz	-	2		Ω
Total gate charge	Qg	V_{DD} = 15V, I_D = 20A, V_{GS} = 10V	-	17	-	nC
Gate - Source charge	Qgs		-	4.1	-	
Gate - Drain charge	Qgd		-	2.5	-	
Turn-ON Delay time	$t_{D(on)}$	V_{GS}=10V,V_{DS}=15V, R_G =3.3Ω, I_D =20A	-	16	-	ns
Turn-ON Rise time	t_r		-	9	-	ns
Turn-Off Delay time	$t_{D(off)}$		-	23	-	ns
Turn-Off Fall time	t_f		-	5	-	ns

Fig.1 Gate-Charge Characteristics

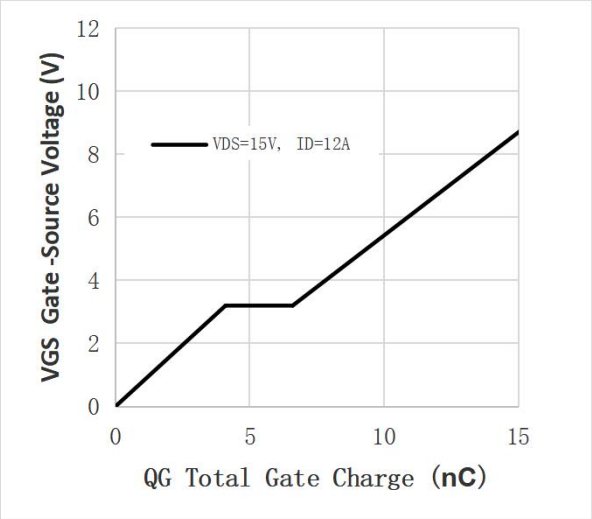


Fig.2 Capacitance Characteristics

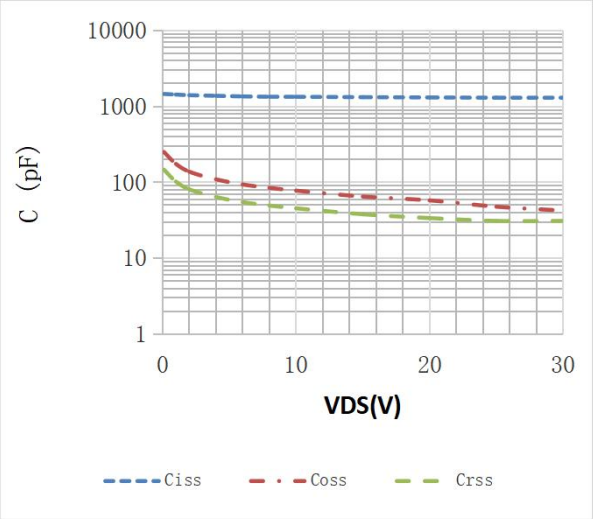


Fig.3 Power Dissipation

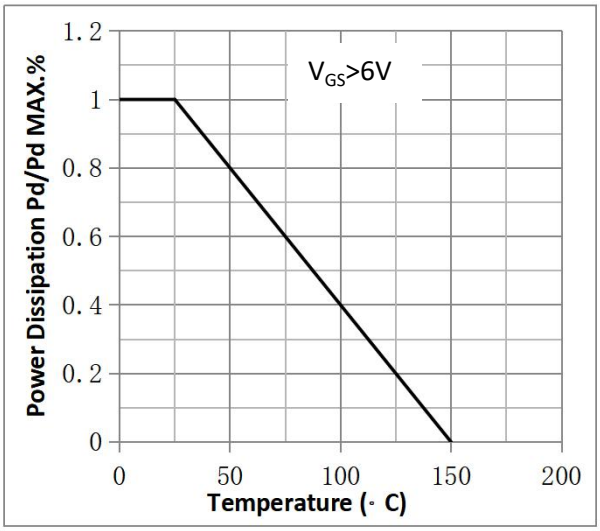


Fig.4 Typical output Characteristics

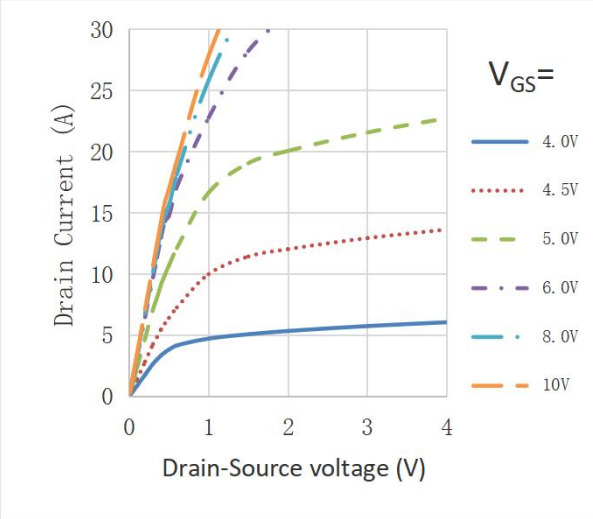


Fig.5 Threshold Voltage V.S Junction Temperature

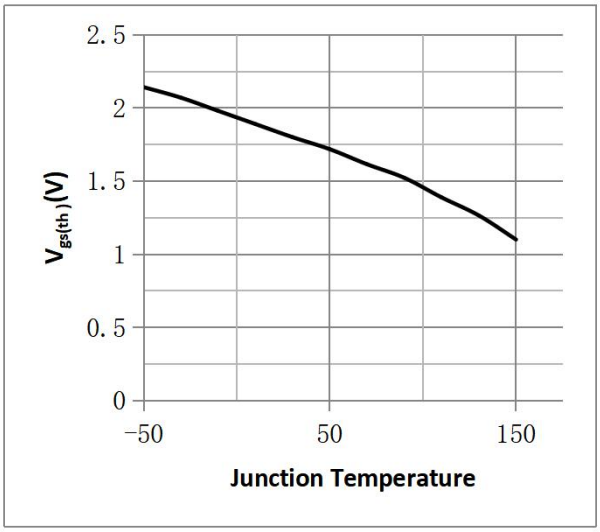


Fig.6 Resistance V.S Drain Current

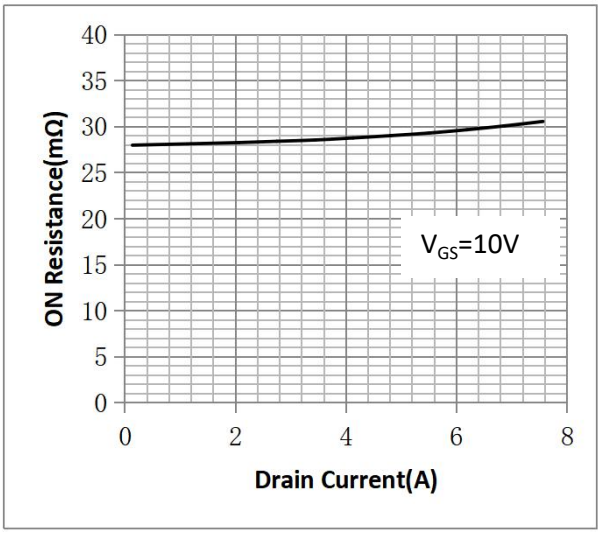


Fig.7 On-Resistance VS Gate Source Voltage

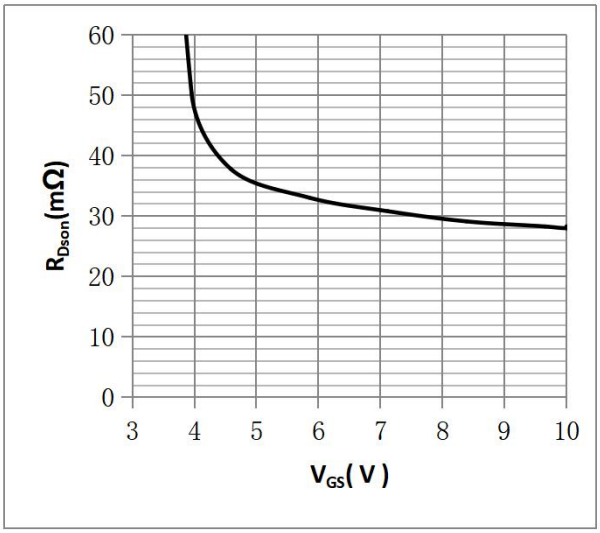


Fig.8 On-Resistance V.S Junction Temperature

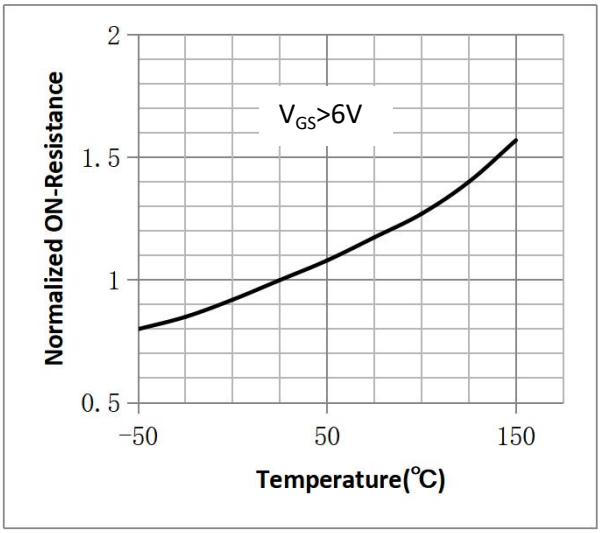


Figure 9. Diode Forward Voltage vs. Current

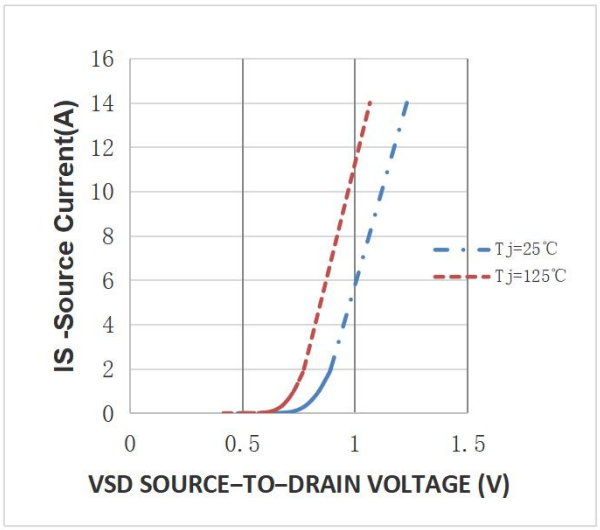


Figure 10. Transfer Characteristics

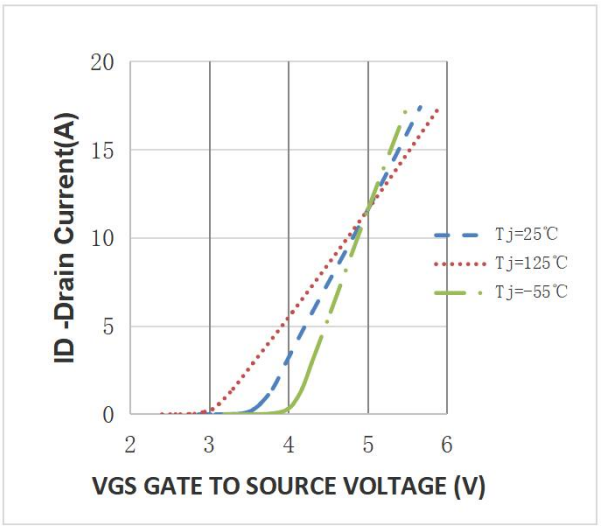


Fig.11 Safe Operating Area

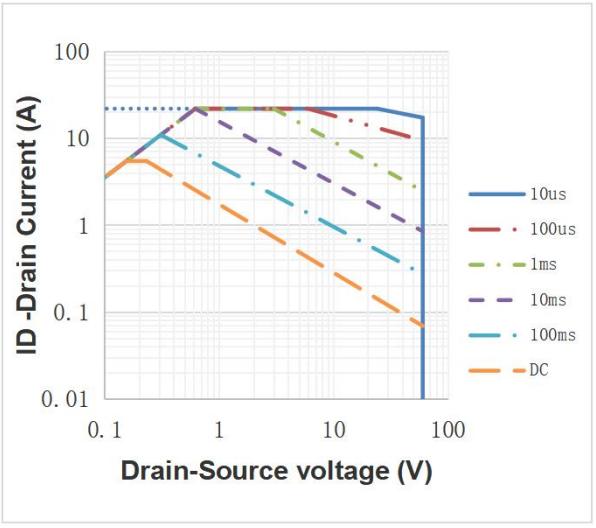
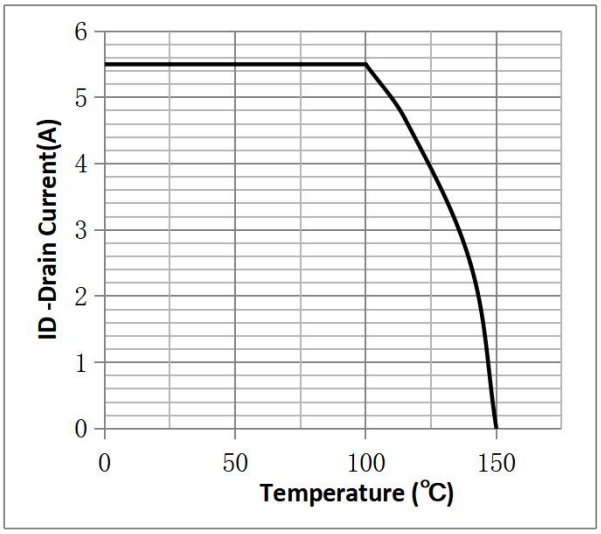
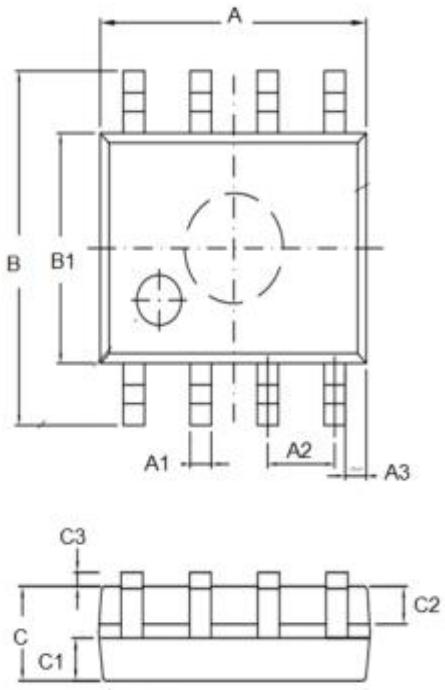


Fig.12 ID vs. Case Temperature②



●SOP-8 Package Outline

SYMBOL	min	TYP	max	SYMBOL	min		max
A	4.8		5.25	C	1.3		1.75
A1	0.37		0.49	C1	0.55		0.75
A2		1.27		C2	0.55		0.65
A3		0.41		C3	0.05		0.2
B	5.8		6.2	C4	0.1	0.2	0.23
B1	3.8		4.1	D		1.05	
B2		5		D1	0.4		0.62



Note:

①Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate；

②Practically the current will be limited by PCB, thermal design and operating temperature. VGS=10V.

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Revision History

Version	Date	Change
A	2021.7.3	NEW
B	2022.5.5	Add Correct ID
C	2023.11.12	1.add Dynamic characteristics 2.Modify Characteristics curve